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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09 686,624	10 12 2000	Jung-Ho Lee	10,253,006	6123

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EXAMINER

NGUYEN, KHIEM D

ART UNIT PAPER NUMBER

2823

DATE MAILED 07 09 2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/686.624

Applicant(s)

LEE ET AL

Examiner

Khiem D Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-37 is/are pending in the application.
- 4a) Of the above claim(s) 1-9 and 29-37 is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 10-12, 14-19 and 22 is/are rejected.
- 7) ☒ Claim(s) 13, 20-21 and 23-28 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 October 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application)
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s) ____
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2-4 6) ☐ Other:

DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of claims 10-28 in Paper No. 6 is acknowledged.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 10-12, 14-19 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over the applicant's admitted prior art (AAPA) of this application in view of Imamura et al. (U.S. Patent 5,738,911), Shibuya et al. (U.S. Patent 6,338,868) and Tanaka et al. (U.S. Patent 5,974,666).

AAPA teaches a method of forming a silicon oxide layer comprising (see Description of the Related Art on pages 1-3 of this application):

providing a semiconductor substrate having a stepped portion formed by at least two conductive patterns selected from gate electrodes and metal wiring patterns of a semiconductor device;

coating the semiconductor substrate with a spin-on glass (SOG) composition;

curing the SOG layer to form a layer of silicon oxide having a planar surface.

AAPA fails to teach that the spin-on glass (SOG) composition containing perhydropolysilazane having the compound formula $-(\text{SiH}_2\text{NH})_n-$ wherein n represents a

positive integer, and main-baking the SOG layer at a temperature within the range of about 400 to about 1,200°C for a second period of time wherein the main-baking is conducted under an atmosphere comprising one or more components selected from the group consisting of oxygen, water vapor, mixtures of oxygen and water vapor, nitrogen, and mixture thereof, for about 10 to about 180 minutes as recited in present claims 10-12, 22.

Imamura teaches that the silicon oxide layer (SiO_2) is forming by baking a spin-on glass (SOG) composition containing perhydropolysilazane having the compound formula $-(\text{SiH}_2\text{NH})_n-$ (n represents a positive integer) in an air atmosphere consisting of oxygen. Wherein the baking of the perhydropolysilazane is conducted by heating at a temperature ranging from 250° C to 500° C for a time ranging from 0.5 to 3 hours. See col. 3, lines 5-34. *It would have been obvious to one of ordinary skill in the art* to incorporate Imamura's teaching into AAPA's method because in doing so a uniform thin film having a thickness of several angstroms can be obtained. See col. 3, lines 28-30

Imamura fails to teach that a weight average molecular weight within the range of about 4,000 to about 8,000, and wherein curing the SOG layer comprises pre-baking the SOG layer at a temperature within the range of about 100 to about 500°C for a first period of time as recited in present claims 10-11 and 23-28.

Tanaka teaches that perhydropolysilazane is heated at 150 °C for a period of time under an oxygen atmosphere to produce silicon oxide layer wherein perhydropolysilazane having a weight-averaged molecular weight of 4,000 to 5000. See col. 6, lines 51-63 and col. 12, lines 16-24. *It would have been obvious to one of*

ordinary skill in the art to incorporate Tanaka's teaching into Imamura's method because doing so can prevent occurrence of leaking based on uneven electric resistance of an electrically conductive elastic body layer. See col. 2, lines 54-61.

Imamura fails to teach that the molecular weight dispersion is within the range of about 3.0 to about 4.0 as recited in present claim 10.

Shibuya teaches coating the surface of a substrate with a spin-on glass (SOG) composition containing polysilazane compound having the molecular weight dispersion not exceeding 4. See col. 3, line 50 to col. 4, line 8. It would have been obvious to one of ordinary skill in the art to incorporate Shibuya's teaching into Imamura's method because in doing so a coating film having high resistance against formation of cracks to serve as a planarizing layer on the surface of a substrate can be obtained. See col. 1, lines 8-16.

Imamura fails to teach the thickness of the silicon oxide layer as recited in present claim 14.

However, it would have been obvious to one of ordinary skill in the art of making semiconductor devices to determine the workable or optimal thickness for the silicon oxide layer through routine experimentation and optimization to obtain optimal or desired device performance because the thickness of the silicon oxide layer is result-effective variables and there is no evidence indicating that the thickness of the silicon oxide layer is critical and it has been held that it is not inventive to discover the optimum or workable range of a result-effective variable within given prior art conditions by routine experimentation. See MPEP 2144.05.

AAPA fails to teach the distance between the at least two conductive patterns is within the range of about 0.04 to about 1 μm as recited in present claim 16

However, it would have been obvious to one of ordinary skill in the art of making semiconductor devices to determine the workable or optimal range for the distance between the at least two conductive patterns through routine experimentation and optimization to obtain optimal or desired device performance because the distance between the at least two conductive patterns is result-effective variables and there is no evidence indicating that the distance between the at least two conductive patterns is critical and it has been held that it is not inventive to discover the optimum or workable range of a result-effective variable within given prior art conditions by routine experimentation. See MPEP 2144.05.

AAPA fails to teach an aspect ratio of the stepped portion between at least two conductive patterns is within the range of about 5:1 to about 10:1 as recited in present claim 17.

However, it would have been obvious to one of ordinary skill in the art of making semiconductor devices to determine the workable or optimal range for an aspect ratio of the stepped portion between at least two conductive patterns through routine experimentation and optimization to obtain optimal or desired device performance because an aspect ratio of the stepped portion between at least two conductive patterns is result-effective variables and there is no evidence indicating that an aspect ratio of the stepped portion between at least two conductive patterns is critical and it has been held that it is not inventive to discover the optimum or workable range of a result-effective

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variable within given prior art conditions by routine experimentation. See MPEP 2144.05

AAPA fails to teach the ranges of the aspect ratio of the stepped portions and the global stepped portion as recited in present claim 18.

However, it would have been obvious to one of ordinary skill in the art of making semiconductor devices to determine the workable or optimal range for the aspect ratio of the stepped portion and the global stepped portion through routine experimentation and optimization to obtain optimal or desired device performance because the aspect ratio of the stepped portion and the global stepped portion are result-effective variables and there is no evidence indicating that the aspect ratio of the stepped portion and the global stepped portion are critical and it has been held that it is not inventive to discover the optimum or workable range of a result-effective variable within given prior art conditions by routine experimentation. See MPEP 2144.05.

Allowable Subject Matter

4. Claims 13, 20-21 and 23-28 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khiem D Nguyen whose telephone number is (703) 306-0210. The examiner can normally be reached on Monday-Friday (8:00 AM - 5:00 PM).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (703) 308-4918. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 746-9179 for regular communications and (703) 746-9179 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

K.N.
July 8, 2002


LONG PHAM
PRIMARY EXAMINER